



Centralized Counselling for M.Tech/M.Arch/M.Plan Admissions

CCMT 2022

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Round Number:4



Personal Details

GATE Registration ID	EC22S35022050	Candidate's Name	BHAVISHYA PATHAK	
Father's Name	Shyam sundar pathak	Mother's Name	Prabha pathak	
Gender	Male	Date of Birth	26-11-1995	
Category	General	GATE Exam Year	2022	
GATE Score	367	GATE Exam Paper	Electronics and Communication Engineering	
GATE Marks out of 100	26.33			

Sub Category

Person with Disability	No
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Seat Allotment Details

Round No.	Choice No.	Allotted Institute Name	Allotted Academic Program	Allotted Category	Group Id
4	5	University of Hyderabad	Microelectronics & VLSI Design	OPEN	G1

Fee Payment Details

Fee Type	Transaction ID	Payment Mode	Amount (Rs)	Payment Date
Special Round Registration and Participation Fee	pay_JwS4K1taXqSrZc	SBI	43000	22/07/2022 08:23:42
Registration Fee Payment	pay_JgclG4ijT5a0jo	SBI	3000	12/06/2022 08:02:38

Qualification Details

Qualifying Degree Marks Details

Passing Status	Passed
Passing Year	2019
Qualifying Degree	Bachelor of Engineering/Technology (BE/B.Tech)
Qualifying Discipline Name	B.E./B.Tech. in Electronics and Communication Engineering
Result Mode	Percentage
Maximum Marks	100
Obtained Marks	61
Percentage Marks	61

Uploaded Documents

Signature
Mark sheet of Class XII
Document for Proof of date of birth
Photo ID proof as per Govt. of India norms
GATE score card
Grade/Mark sheets of qualifying examination for all semesters,if awaited then uptill pre-final semester
Degree/Provisional certificate/Course completion certificate

Document Verification Details

Remarks	OK
Changes During Verification	NONE

Document Verifying Officer
(MR AVINASH)

Center Incharge
(DR BIPIN P VARGHESE)